

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/766845	F	324	763	2858 2699	P. Patel
Kiyotoshi Ueda Shoichi Ooshita					
Method and apparatus for testing semiconductor integrated circuit, and semiconductor integrated circuit manufactured thereby					
PTO-2040 12/89					

ISSUING CLASSIFICATION			
ORIGINAL		CROSS REFERENCE(S)	
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)
INTERNATIONAL CLASSIFICATION			
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